## REMARKS

The Examiner objected to claims 12, 15, 16, 19 and 25 stating "Claims 12, 15, 16, 19 and 25 are objected to because of the following informalities: to improve clarity, in claims 12 and 19, line 3, it appears as if "by' should be inserted after "divided" and in line 6, "where" should be inserted after "number", in claim 15, "on-shot" should be replaced with "one-shot", in claim 16, "said clock signal" should be replaced by "said input clock signal" and in claim 25, line 5, "one shot generated" should be "one-shot generator". Further, only first and second latches are recited in the second paragraph of claim 25 but a next to last latch, a last latch and a first latch (implying three latches) are recited in the next paragraph. This inconsistency creates confusion such that Applicant has failed to particularly point out the claimed invention.

In response, Applicants have amended claims 10, 12, 15-19, 23 and 25 by complying with the Examiners suggested corrections to overcome the Examiners 35 U.S.C. 112, second paragraph objections.

The Examiner has stated "Claims 10 and 17 would be allowable if rewritten or amended to overcome the rejection(s) under 35 U.S.C. 112, 2nd paragraph, set forth in this Office action. Claims 11-16 and 18-25 would be allowable if rewritten to overcome the rejection(s) under 35 U.S.C. 112, 2nd paragraph, set forth in this Office action and to include all of the limitations of the base claim and any intervening claims." Applicants gratefully acknowledge the Examiner's indication of allowable subject matter.

The Examiner rejected claims 10-25 under 35 U.S.C. 112 (2nd paragraph) stating "Claims 10-25 are rejected under 35 U.S.C. 112, second paragraph, as being indefinite for failing to particularly point out and distinctly claim the subject matter which applicant regards as the invention. Proper antecedent basis has not been provided for the following terms: In claim 10, "the output of a next to last latch", in claim 15, "the input", "said inverting multiplexer" and "the

outputs", in claim 16, "the output of said NAND stage" and "said latch"., in claim 17, "the output of a next to last latch", in claim 18, "the data output of a next to last latch, in claim 23, "the output of said NAND stage" and in claim 25, "said last latch".

In response, Applicants have amended claims 10, 15-18, 23, and 25 to overcome the Examiners 35 U.S.C. 112, second paragraph objections rejections correcting the improper antecedent basis terms pointed out by the Examiner.

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## CONCLUSION

Based on the preceding arguments, Applicants respectfully believe that all pending claims and the entire application meet the acceptance criteria for allowance and therefore request favorable action. If Examiner believes that anything further would be helpful to place the application in better condition for allowance, Applicants invite the Examiner to contact the Applicants' representative at the telephone number listed below. The Director is hereby authorized to charge and/or credit Deposit Account 09-0457.

Dated: 01/13/2005

Respectfully submitted, FOR: Austin et al.

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